ECN#

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## \_fwirlight\_

## **ENGINEERING CHANGE NOTE:**



ECN #:

ASSEMBLY: BI New Assembly: Current Assy Rev: D New Assy Rev: 11			MW4-QDC Quad Digital Channel Card 05/06/00
CMS BOMs Updated	Excel BOMs Updated	Subcontractor Records Updat	ted Compatibility Maintained: Yes
IMPLEMENTATION INFORMATION:			
<ul><li>○ Safety Issue</li><li>○ Bug Fix</li><li>○ Reliability Reasons</li></ul>	<ul><li>○ UL Compliance Issue</li><li>○ EMC Compliance Issue</li><li>○ Software Change</li></ul>	<ul><li>New Feature</li><li>Quality Issue</li><li>Cosmetic Change</li></ul>	○ Cost Reduction
REASON FOR CHANGE:  Enable software to identify the boards with 25ns speed DPRAMs			
DETAILS OF CHANGE:			

Inspect ICs U112-U115 and U123-U126 (8 x CY7C024 DPRAMS)

If any of these 8 parts have a speed grade = 25 ns then add a solder blob across J5 and place a assembly revision label after the blank PCB P/N on the card, marked "1D", so it looks as follow: "BMW40413-1D"

If all 8 parts are speed grade 15 or 20 ns then there should be no solder blob on J5 and place a assembly revision label after the blank PCB P/N on the card, marked "D", so it looks as follow: "BMW40413-D"